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MAY 12 2006**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants: Jeffrey Scott Kuskin, et al. : Group
Serial No: 09/662,991 : Art Unit #2136
Filed: 15 September 2000 : Examiner:
Title: KEY CACHING SYSTEM : C.G. Colin

SUPPLEMENTAL DECLARATION OF TAO-FEI SAMUEL NG
UNDER 37 C.F.R. § 1.131

Mail Stop – AF
Honorable Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

I, Tao-Fei Samuel Ng, do hereby declare as follows:

1. I acknowledge and re-assert herein my statements in my Declaration of 29 September 2005 earlier submitted in the above-referenced Patent Application. The same Exhibits A and B referenced in that Declaration are again attached to this Supplemental Declaration and referenced for convenience by the same Exhibit designations, Exhibit A and Exhibit B.

2. The other co-Inventors and I actually reduced the subject matter of our invention, which I understand to be disclosed and claimed in this Application, in this Country (the United States of America) to the point of actual test integration sometime before July of 2000.

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3. Exhibits A and B were provided as evidence of such reduction to practice.

4. Exhibit A is a copy of an excerpted portion of a confidential hardware specification document for a developmental system which, in part, integrated a version of our invention.

5. This Exhibit A is a true and accurate copy of such hardware specification document internally distributed within Atheros Communications, Inc. (Assignee of the above-referenced Patent Application), located in Sunnyvale, California on or about 5 May 2000.

6. Among the integrated features reflected in this Specification document is that of "the transmission of a uni-cast non-control frame and a RTS frame," after which the "receiver is expected to respond with an ACK and CTS frame respectively" (lines 51-52). As the document specifies, if the "expected response was not forthcoming within a Time-out period, a reception error is assumed" (lines 53-54). This is consistent with "acknowledgment-responsive wireless communication with the external source" mentioned in Claims 1, 7, and 13 of the present Patent Application.

7. At lines 200-227, the document briefly describes certain aspects of operation with encrypted transmission and use of an encryption key and key cache, among other things. The document makes mention of a "key cache which stores keys that are shared between the station with other stations," (lines

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204-205), and explains that a receiving frame is examined "to check for encryption," whereupon "the KeyID field of the WEP extension is read" (lines 217-218). As the document further notes, an appropriate search of the "key cache" is made for the appropriate key "used to decrypt the frame body;" there being the option to "disable the sending of ACK responses for encrypted frames for which no key can be found" (lines 219-225; 225-227).

8. Exhibit B is a true and accurate copy of a brief written summary internally distributed within Atheros on or about 21 June 2000, which addresses this part of system operation in more detail. The summary notes that the key caching scheme employed "takes advantage of such a positive acknowledgement based system by NOT returning the acknowledgement for received data if the source address was not found in the active table" (paragraph 4, lines 1-2).

9. The summary goes on to explain that:

This gives time for the receiving system to examine its stored table to see if the required tuple exists there. If not, the system may either continue to ignore the data, by not returning an acknowledgement, or program a fake tuple into the active table so that an acknowledgement is returned before discarding the data.

(paragraph 4, lines 2-5).

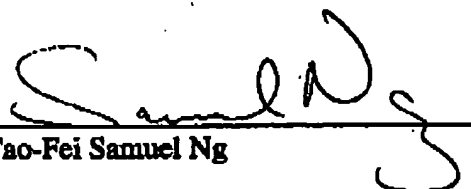
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10. It is my understanding that the Claims of the subject Patent Application read on such features.

I declare further that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true, and further, that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the Application, or any Patent issuing thereon.

Signed this 10 th day of April, 2006.


Tao-Fei Samuel Ng